PATENT Docket No. JCLA7022 page 1

IN TH	E UNITED	STATES	PATENT	AND TR	ADEMARK	OFFICE
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In re application of :

Application No.

: 10/039,852

Filed

: October 22,2001

For

: METHOD AND CIRCUIT FOR TESTING A CHIP

Examiner

: TRIMMINGS, JOHN

Art Unit

: 2133

## TRANSMITTAL SHEET

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NOV 1 2 2004

Commissioner for Patents P.O. BOX 1450

**ALEXANDRIA, VA 22313-1450** 

Sir:

Transmitted herewith is an Amendment with amended drawings in (15) pages.

(X) An extension of time to respond for (3) month(s) is hereby requested.

Time Extension Fee:

() One month

(\$110 for Large Entity)

() Two months

(\$430 for Large Entity)

(X) Three months

(\$980 for Large Entity)

(X) Commissioner is authorized to charge the three-month time extension fee in the amount of \$ \_980\_ as calculated above or any other fees required in connection with the filing of this paper to Deposit Account No. 50-0710 (Order No. JCLA7022).

FEE VALUE COOP PURMISHED 12 53 980-

10039852

FIEE VALUE ACCOUNTABILITY

DEPOSIT ACCOUNT NO

50

Date: 11/12/2004

Please send Correspondence To:

J.C. Patents

4 Venture, Suite 250

fryine, California 92618

Tel: (949) 665-0761

2885 HS Ref: 888

HSMITH1

01/13/2005 HSMITHI 00000001 500710

PAGE 2117 \* RCYG AT 11(12)7304 7 28:07 PM [Eastern Standard Think] \* SVR:USPTO-EFXRF-U1 \* DKIS:8729305 \* CBID: 19495603609 \* DURATION (mm-4s):04-38

01 FC+1253

Jiawei Huang

Registration No. 43,330

980.00 DA